*Describe what you did. This does not mean that you copy and paste from what you have posted or the assignments you have prepared. You need to describe what you did and how you did it.*

* As well as the last unit, I started from reading through textbooks this week and learnt many new concepts about binary calculations. The concept of complement or signed binary calculation was not easy for at first, solving self-quiz helped me understand those new concepts. Then, I repeated the quiz until I do not encounter new questions and understand all questions. Then, I wrote discussion forum and completed programming assignments to practice the concept of this unit. Finally, I am writing learning journal right now.

*Describe your reactions to what you did*

* I am satisfied with what I have done this unit, since there were many new learnings for me this unit, starting from how to structure complex gates with primitive gates to the idea of cost effectiveness of gate implementation. Although I want to get some chance to write a code or experience more technical activity, so far so good.

*Describe any feedback you received or any specific interactions you had. Discuss how they were helpful.*

* I received several feedbacks from my peers and instructors. Generally, all works are highly rated, and I am satisfied with their evaluations. But there is one thing that I could do more; posting discussion forum in early timing of the week and have enough time to get replies from my peers. Since I posted in the very late timing, I was not able to get any replies, which is not good as a “discussion” forum. Though, it has been very hard for me to balance a schedule of assignments and full-time work, I am going to try complete discussion forum as soon as possible.

*Describe your feelings and attitudes*

* My feeling and attitudes continue to stay very positive since I am satisfied with my learning activity so far.

*Describe what you learned*

* My biggest learning this week is the usage of NAND gate to make more efficient NXOR gate in the activity of discussion forum. I was not aware of the high performance of NAND gate, so I just used 4 gates to implement the original NXOR gate. Although I am still confused about the internal details of why NAND gate performs more efficiently than the combination of primitive NOT and AND gate, it was a good learning.

*What surprised me or caused me to wonder?*

* As I state in the last question, learning the internal detail of gates in physical or electric level is very challenging for me and it causes the difficulty to understand the full content of this unit.

*What skills and knowledge do I recognize that I am gaining?*

* I recognize myself gain the most fundamental part of computer science. Although it is still difficult to connect what I am learning in this unit and what I am doing in the daily job as an infra engineer since I am not hardware engineer, I believe these knowledges must be a basic of my ability as an engineer. So, I am going to continue to work hard.